

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
79	BRS	366	parallel same calculation and operating same point and circuit and potential	USPAT; US-PGPUB; DERWENT	2003/09/24 16:00		
80	BRS	34	parallel same calculation and operating same point and circuit and potential and convergence	USPAT; US-PGPUB; DERWENT	2003/09/24 16:10		
81	BRS	25493	operating same point and electrical same circuit	USPAT; US-PGPUB; DERWENT	2003/09/24 16:10		
82	BRS	206	operating adj point and electrical adj circuit and computer	USPAT; US-PGPUB; DERWENT	2003/09/24 16:11		
83	BRS	12	operating adj point and electrical adj circuit and computer and partition	USPAT; US-PGPUB; DERWENT	2003/09/26 11:40		
84	IS&R	1	("6553524").PN.	USPAT; US-PGPUB	2003/09/25 14:35		
85	BRS	14	operating adj point and partition and parallel same calculation and circuit	USPAT; US-PGPUB; DERWENT	2003/09/26 11:41		
86	BRS	59	operating adj point and partition and parallel and circuit and capacitance and node and potential	USPAT; US-PGPUB; DERWENT	2003/09/26 12:18		
87	BRS	79	analog same parallel same algorithms and simulation	USPAT; US-PGPUB; DERWENT	2003/09/26 12:19		
88	BRS	1	analog same parallel same algorithms same simulation and circuit and partition	USPAT; US-PGPUB; DERWENT	2003/09/26 12:20		
89	BRS	12	analog same parallel same algorithms same simulation and circuit	USPAT; US-PGPUB; DERWENT	2003/09/26 12:23		
90	BRS	1841587	parallel simulation same transistor	USPAT; US-PGPUB; DERWENT	2003/09/26 12:23		
91	BRS	3	parallel adj simulation same (transistor or analog)	USPAT; US-PGPUB; DERWENT	2003/09/26 12:36		
92	BRS	0	parallel adj simulation same transistor and capacitance	USPAT; US-PGPUB; DERWENT	2003/09/26 12:36		
93	BRS	0	parallel adj simulation same transistor and capacitance	USPAT; US-PGPUB; DERWENT	2003/09/26 12:36		
94	BRS	1	parallel adj simulation same transistor and operating same point	USPAT; US-PGPUB; DERWENT	2003/09/26 12:40		

	Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
95	BRS	2	parallel adj simulation same transistor	USPAT; US-PGPUB; DERWENT	2003/09/26 12:41		
96	BRS	0	parallel adj simulation same capacit\$4 and analog	USPAT; US-PGPUB; DERWENT	2003/09/26 12:42		
97	BRS	2	parallel adj simulation same capacit\$4	USPAT; US-PGPUB; DERWENT	2003/09/26 12:42		
98	BRS	7	parallel adj simulation and capacit\$4 and analog	USPAT; US-PGPUB; DERWENT	2003/09/26 12:56		
99	BRS	535	parallel same simulation and capacit\$4 and analog	USPAT; US-PGPUB; DERWENT	2003/09/26 12:56		
100	BRS	103	parallel same simulation same capacit\$4 and analog	USPAT; US-PGPUB; DERWENT	2003/09/26 12:56		
101	BRS	52	parallel same simulation same capacit\$4 same circuit and analog	USPAT; US-PGPUB; DERWENT	2003/09/26 12:57		
102	BRS	100	parallel same simulation same capacit\$4 and analog and circuit	USPAT; US-PGPUB; DERWENT	2003/09/26 14:31		
103	BRS	2405	operating same point same analysis	USPAT; US-PGPUB; DERWENT	2003/09/26 14:31		
104	BRS	351	operating adj point same analysis	USPAT; US-PGPUB; DERWENT	2003/09/26 14:31		
105	BRS	234	operating adj point same analysis and circuit	USPAT; US-PGPUB; DERWENT	2003/09/26 14:31		
106	BRS	100	operating adj point same analysis and circuit and transistor	USPAT; US-PGPUB; DERWENT	2003/09/26 14:32		
107	BRS	33	operating adj point same analysis and circuit and transistor and dynamic	USPAT; US-PGPUB; DERWENT	2003/09/26 14:47		
108	BRS	5	brasil and simulator	USPAT; US-PGPUB; DERWENT	2003/09/26 14:48		
109	BRS	0	brasil and simulator and integrated adj circuit	USPAT; US-PGPUB; DERWENT	2003/09/27 05:35		

	Errors
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96	0
97	0
98	0
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104	0
105	0
106	0
107	0
108	0
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90	0
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